

## REMARKS

Re-examination and allowance of the present application is respectfully requested.

Initially, Applicants thank the Examiner for returning a completed copies of the PTO-1449 Forms that accompanied the various filed Information Disclosure Statements. However, Applicants note that the Examiner did not indicate his consideration of the English language Abstract for Japanese document HEI 10-268831 with respect to the Information Disclosure Statement that was filed on July 21, 2001, stating that a copy of the Abstract was not provided. Applicants note that a copy of the Abstract was submitted with the Information Disclosure Statement that was filed on March 8, 2004, and thus, it was not necessary to submit a second copy of the document to the Examiner, as indicated on page 2 of the July 21, 2004 Information Disclosure Statement. In any event, Applicants note that the Examiner indicated his consideration of the English language Abstract on the PTO-1449 Form that accompanied the March 8, 2004 Information Disclosure Statement. Accordingly, the Examiner is respectfully requested to re-confirm his consideration of this English language Abstract.

Claims 1-18 stand rejected under 35 U.S.C. §103(a) as being obvious over U.S. Patent 6,011,355 to NAGAI. In setting forth this rejection, the Examiner acknowledges that NAGAI fails to disclose/suggest a frequency reducer, but

asserts that the document teaches that inductance values may be varied to obtain a desired resonance frequency. Applicants respectfully traverse this ground of rejection.

Applicants respectfully submit that NAGAI fails to disclose or suggest the connection of a capacitance device in parallel with the switching devices to shift a resonance frequency of an LC resonance generated at the time of a transition of the transistors from an OFF state to an ON state, as specified in Applicants' claims. For example, Applicants submit that NAGAI does not disclose, at least, that capacitances C1 and C2 are positioned in parallel with the drain-source of transistors Q1 and Q2, as taught by an embodiment of the presently invention. In particular, Applicants submit that NAGAI does not disclose (or even suggest) that a frequency reducer is connected in parallel with a switcher that is operable to reduce a resonance frequency of an LC resonance that results from a parasitic capacitance of the switcher and an inductance component of an interconnector, by specified by the claims of Applicants' invention. Accordingly, Applicants submit that the claims of the present invention are not obvious over NAGAI, and respectfully request withdrawal of the 35 U.S.C. §103 rejection, and an indication of the allowability of the pending claims.

Furthermore, Applicants submit that NAGAI fails to disclose or suggest that the frequency reducer has a capacitive element connected in parallel with the

switcher to reduce a resonance frequency of an LC resonance resulting from a parasitic capacitance of the switcher and an inductance component of the interconnector, as specified in, for example, claims 3, 7, 10, 14, 16 and 18.

Accordingly, Applicants submit that an additional ground exists for concluding that these claims are allowable over the applied art of record, and respectfully requests such an indication from the Examiner.

Still further, Applicants submit that NAGAI fails to disclose or suggest a first interconnector that is connected to a power supply, and a second interconnector that is connected to a protector and the first interconnector, in which the frequency reducer is operable to reduce a resonance frequency of an LC resonance that results from a parasitic capacitance of the protector and inductance component of the second interconnector, as specified in, for example, claims 4 - 7 and 11 - 14. In this regard, Applicants note that claims 7 and 14 additionally specify that the frequency reducer has a capacitance element. Accordingly, for at least these additional reasons, Applicants submit that an additional ground exists for concluding that these claims are allowable over the applied art of record.

Further, Applicants note that NAGAI was cited by the European Examiner in a counterpart European application to be a category "A" document, merely disclosing the state of the art. Applicants submit that this document is no more

relevant with regard to the U.S. pending claims, than it was with respect to the claims in the counterpart European application.

SUMMARY AND CONCLUSION


In view of the fact that the art of record, whether considered alone or in combination, fails to disclose or suggest the present invention as defined by the pending claims, and in further view of the above remarks, reconsideration of the Examiner's action and allowance of the present application are respectfully requested and are believed to be appropriate.

Should the Commissioner determine that an extension of time is required in order to render this response timely and/or complete, a formal request for an extension of time, under 37 C.F.R. §1.136(a), is herewith made in an amount equal to the time period required to render this response timely and/or complete. The Commissioner is authorized to charge any required extension of time fee under 37 C.F.R. §1.17 to Deposit Account No. 19-0089.

If there should be any questions concerning this application, the Examiner is invited to contact the undersigned at the telephone number listed below.

November 7, 2005  
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